Q series high-speed counter HCQX-HC04-D2

HPPP1380000EN

1.3

Apr,2022

ManualNo. HC_{04} Version

Thank you for purchasing and using the Q series high-speed counter modules independently developed and produced by HCFA Corporation This manual will give the brief explanation for the following modules

in the table:				
Name	Module	Version	Power	Description
High- speed counter module	HCQX- HC04-D	V1.0	2.7w	The high-speed counter needs to be connected to the end of local extension modules or the coupler. It cannot be used alone and supports multiple counting functions.
High- speed counter module	HCQX- HC04- D2	V1.0	2.7w	The high-speed counter needs to be connected to the end of local extension modules or the coupler. It cannot be used alone and supports multiple counting functions.

When the user selects modules according to the power, part of the power is reserved to avoid the loss during the signal transmission. TIPS:

Applicable readers

For the users of HCFA Q series extension modules, refer to this manual to perform the wiring, installation, diagnosis and maintenance and requires the users to have the certain knowledge of electrical and automation.

This manual gives the necessary information for the use of HCFA Q series extension modules, please read this manual carefully before use and make the correct operation with full attention to safety.

1. Safety precautions

1.1Safety icons

When using this product, please follow the following safety guidelines and strictly follow the instructions $% \int_{\Omega}^{\Omega} \left(\int_{\Omega} \left(\int_{\Omega$

Users can see more detailed and specific safety guidelines in sections such as DIN rail mounting, wiring, communication, etc.

A DANGER

Indicates that incorrect handling may cause hazardous conditions
resulting in death or severe injury or significant property damage

 ndicates that incorrect handling may cause hazardous condition resulting in medium or slight personal injury or physical damage.

Indicates that incorrect handling may cause slight injury or property damage

Indicates that incorrect handling may cause damage to the environment/equipment or data loss.		
TIPS: Key points or explanations to help with better operation a	Ind	

1.2 Safety rules

STARTUP AND MAINTENANCE PRECAUTIONS

• Do not touch any terminal while the PLC's power is on. Doing so may cause electric shock or malfunctions. Before cleaning or retightening terminals externally cut off all phases of the power supply. Failure to do so may cause electric

, shock. Before modifying or disrupting the program in operation or Forced output, RUN, STOP etc., carefully read through this manual and the associated manuals and ensure the safety of the operation. An operation error may damage the machinery of cause accidents.

An operation error may damage the machinery or cause accidents

Startup And Maintenance Precautions

• Do not disassemble or modify the PLC. Doing so may cause fire, equipment failures, or malfunctions.For module repair, contact our HCFA distributor.

Turn off the power to the PLC before connecting or disconnecting any extension cable. Failure to do so may cause equipment failures or malfunctions

Turn off the power to the PLC before attaching or detaching the following devices. Failure to do so may cause equipment failures or malfunctions

-Display module, peripheral devices, expansion boards -Extension blocks and special adapters

-Battery, terminal block and memory cassette

Disposal Precautions

Transport And Storage Precautions

Please contact a certified electronic waste disposal company for the environmentally safe recycling and disposal of your device

• The PLC is a precision instrument. During transportation, avoid impacts larger than those specified in Section 3.1. Failure to do so may cause failures in the PLC. After transportation, verify the operations of the PLC.

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D2

2. Product overview





Figure 1 Model name and nameplate description

Model name 2 Module QBUS power consumption

Input parameters
Output parameters
Code, S/N & P/N

6 QR code (model name, serial number)

2.2 Part name description



No. Name Functions (1) Indicators Used to display module and terminal status Mounting (2) Fix the connector on the module hook Transmit QBUS signal and control circuit current, do not support hot swap (3) Sheet metal (4) Terminal Insert the cable, input/output signal

■ Indicator arrangements for HCQX-HC04-D2

	Description		NC)	Description
QX-HC04-D2	A1	0		10	B1
	Z1	1		11	11
P RUN	A2	2		12	B2
A1B1Z1I 1	Z2	3		13	12
A2_B2_Z2_I2_	A3	4		14	B3
A3B3Z3I 3	Z3	5		15	13
A4B4Z4I4	A4	6		16	B4
	Z4	7		17	14
	SS	8		18	SS

Table 2 Part names and function description -2		
Symbol	Indicator color	Channel description
Ρ	Green	The indicator show the current power supply status of the module.
RUN	Red	ESC Normal operation indicator, Lit means the module is in normal running state
A1	Red	Input channel 1 detects the encoder phase A input signal
B1	Red	Input channel 1 detects the encoder phase B input signal
Z1	Red	Input channel 1 detects the encoder phase Z input signal
11	Red	Input channel 1 detects the input signal of functional terminal I1
A2	Red	Input channel 2 detects the encoder phase A input signal
B2	Red	Input channel 2 detects the encoder phase B input signal
Z2	Red	Input channel 2 detects the encoder phase Z input signal
12	Red	Input channel 2 detects the input signal of functional terminal I2
A3	Red	Input channel 3 detects the encoder phase A input signal
B3	Red	Input channel 3 detects the encoder phase B input signal
Z3	Red	Input channel 3 detects the encoder phase Z input signal
13	Red	Input channel 3 detects the input signal of functional terminal I3
A4	Red	Input channel 4 detects the encoder phase A input signal
B4	Red	Input channel 4 detects the encoder phase B input signal
Z4	Red	Input channel 4 detects the encoder phase Z input signal
14	Red	Input channel 4 detects the input signal of functional terminal I4
SS	No indicator	S/S common terminal
SS	No indicator	S/S common terminal

2.2.2 Right view for HCQX-HC04-D2



Figure 3 Interface diagram from right view for HCQX-HC04-D2

Name	Functions
Sheetmetal	Transmit QBUS signal, transmit control circuit current, do not support hot swap
Mounting hook	Fix the module on the DIN rail
Connector	Provide hot-swappable wiring device to facilitate user wiring and modulereplacement
Connector mounting hook	Fix the connector on the module
Cable tie	Pass the cable on the module and fix it with a tie to make the wiring more tidy and beautiful, and convenient for later maintenance

2.3 Product Dimensions

Product Dimensions



3.2 Environment specifications

Items	Specifications
Working temperature	0~55℃
Storage temperature	-25~85℃
Relative humidity	95%, No condensation
Altitude	2km or less
Atmosphere	108kPa~66kPa
Noise	±2kV, 5~100kHz
Sinusoidal vibration	9Hz <f<100hz, 1.0="" acceleration,="" amplitude<="" constant="" td=""></f<100hz,>
drop down	1m, 10 times during packaging and transportation

Items	Specifications
QBUS rated power	12Vdc±5%
QBUS max. current consumption	70mA
Rated power for IO terminal	24Vdc
Input voltage range for IO terminal	20.4Vdc~28.8Vdc

Str Elle differ specifications			
Specifications			
24Vdc/8.4mA			
DC15V or more/5mA or more			
200Khz			
Less than 2us			

3.5 Input specifications

3.7 Interface specifications

Items	Specifications		
Communication Interface	QBUS_IN, QBUS_OUT		
Communication interface type	10/100BASE-TX (IEEE 802.3)		
Input interface	16 points, 2 common terminals		
	Power Indicator: P green (Indicate power supply)		
LED indicators	Operation indicator: RUN red (Indicate module OP status)		
	Channel indicator: A1-4, B1-4, Z1-4, I1-4 red (Indicate input status)		
Debugging	T20F256C4	JTAG	
Debugging	STM32F407	SWD, UART	
Interface	LFE5U-12	JTAG	

4. Installation instruction

4.1 Installation instruction

4.1.1 Control cabinet installation

When installing in the control cabinet, please pay attention to the following:

- Install the module in the vertical direction. Use natural air or fan cooling to the device. And install the module on the 35mm DIN rail by the mounting hooks.
- (2) The cooling fan or natural air need to reduce the temperature as below to leave enough space around the equipment. To prevent the ambient temperature of the device from being too high, keep the temperature in the electric cabinet even



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2 After the installation is completed, as shown in the figure below:



• Module dismounting

When you need to disassemble, you should press the module with both hands (shown in the direction of the arrow in the figure), and pull out the module vertically upwards.



	Specifications
emperature	0~55℃
mperature	-25~85℃
imidity	95%, No condensation
	2km or less
re	108kPa~66kPa
	±2k∨, 5~100kHz
vibration	9Hz <f<100hz, 1.0="" acceleration,="" amplitude<="" constant="" td=""></f<100hz,>
1	1m, 10 times during packaging and transportation

Items	Specifications
QBUS rated power	12Vdc±5%
QBUS max. current consumption	70mA
Rated power for IO terminal	24Vdc
Input voltage range for IO terminal	20.4Vdc~28.8Vdc

3.4 Line driver specifications

Items	Specifications
Collector input	24Vdc/8.4mA
ON-voltage/ON-current	DC15V or more/5mA or more
Single-phase maximum response frequency (A/B phase)	200Khz
ON/OFF response time	Less than 2us

3.3 Power supply input specifications

Items	Specifications
QBUS rated power	12Vdc±5%
QBUS max. current consumption	70mA
Rated power for IO terminal	24Vdc
Input voltage range for IO terminal	20.4Vdc~28.8Vdc



Figure 4 Installation dimension for HCQX-HC04-D2 (unit: mm)

3. Specification

3.1 Electrical specifications

Items	Test Conditions	Remarks				
Dielectric strength	Input to output	AC 500V 60s				
Insulation resistance	Input to output	1ΜΩ				
Leakage current	(input to shell)	1mA				
EMC	Electrostatic discharge	Contact ±4kV, air ±8kV				
	Electrical fast	Control power	±4kV, 5&100kHz			
	burst	Network cable	±2kV, 5&100kHz			
	Surge	DC500V				

Iterna	opeenications
Number of channels	4
Number of inputs per channel	4
Rated input voltage	24Vdc (20.4Vdc~28.8Vdc)
Input resistance	3kΩ
Input type	NPN /PNP
Wiring method	Three-wire encoder
Pulse input method	Orthogonal phase pulse (x2/4)/pulse plus direction/up and down pulse
Counting unit	Pulse
0	21/7/02//0 21/7/02//7

3.6 Counter function

Items	Specifications
Counter type	Ring counter or linear counter
Counter control	Gate control, counter reset and counter preset
Latch function	1 external input latch and 1 internal latch
Measurement method	Pulse rate measurement and pulse period measurement

(3) When installing side by side, it is recommended to leave space of more than 10mm on both sides of the horizontal direction (if the installation space is limited, you can choose not to leave space)



4.1.2 Module mounting and dismounting

Module mounting

 $\fbox{1}$ Align the HCQX-HC04-D (the area indicated by the dots) with the Q-series main unit (the area indicated by the dots). At this time, the installation of the HCOX-HC04-D machine is completed (Make sure the mounting hook is in a retracted state before installation, otherwise it may cause installation failure).



4.1.3 (Un)Installation of guide rails

Rails installation

1 Align the bottom of HCQX-HC04-D with the 35MM international guide rail, and then press down hard, when you can hear a "click", it indicates that the bottom of the mounting hook has been connected to the international guide rail. Then the HCQX-HC04-D installation completed (Before installation, ensure that the mounting hook is in good state, otherwise it may cause installation failure)

Press firmly on the bottor Snap top anging 0

2 After the installation is completed, as shown in the figure below



Rails uninstallation

First remove the 35MM international guide rail dovetail groove fixing parts installed on the two sides of the machine, and then pull upwards at a distance of about 5.8 mm (when you pull upward, you can clearly hear the "click"), at this time you can directly take off the machine to complete the disassembly (you can use the accessories, such as screwdrivers, etc., when pulling)



4.1.4 (Un)installation of connector

Installation of connectors

Align the bottom of the connector with the bottom of the extension module. After aligning, press down on the top of the terminal in the direction shown in the figure below. When you hear a "click", the assembly of the connector is completed.



• Connector uninstallation

1 Use your index finger or your middle finger to firmly press the top spring of the connector down to separate the top of the connector from the extension module, and use your thumb to press the rear part of the connector. While pressing, lift the top of the connector upwards and take it away.



2 Lift the top of the connector to make the connector and the extension module at an angle greater than 45°, and finally remove the connector in upward direction $\hfill \ensuremath{\mathbb{T}}$



4.1.5 (Dis)connection of cables

- (Dis)connection of cables
- 1 First insert a small screwdriver into the hole, apply a force of 10N inward, and then insert the cable into the hole. Pull out the small screwdriver after the cable is inserted.



2 After the installation is complete, gently pull out the cable, the installation is complete if the cable does not fall off.



Cable disconnection

Insert a small screwdriver into the hole, apply a force of 10N inward, then pull out the cable, and finally take out the screwdriver.



4.2 Wiring description

4.2.1 Cable selection

Item		Specifications					
Installatio	n method	Push-in					
Push force	e (per contact)	10N					
Cable typ	е	Copper wire only (aluminum cable is not allowed)					
Cable len	gth	7~9 mm					
C	Single-stranded	0.08~1.50 mm2/28~16 AWG					
Cross	Multi-stranded	0.25~1.50 mm2 /24~16 AWG					
300000	Sleeve	0.25~0.75 mm2 /24~20 AWG					

4.2.2 Internal wiring description

Input internal circuit



♦ Output internal circuit



4.2.3 External wiring description



♦ Wiring precautions

When wiring the I/O module, please note the following:

• Distinguish the input/output cables and make the wiring separately. Distinguish the input/output cables and make the winnig separately.
 If the power cable is close to I/O signal cable, error may occur because of high-voltage/current. The distance between I/O signal cable and power cable should be more than 100mm.
 24VDC I/O cable should be laid separately from AC power cable.

When using piping for wiring, make sure that the piping is wellgrounded.

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5. Module programming examples

This example uses the CPU unit HCQ1-1300-D + coupler module HCQX-EC + high-speed counter module HCQX-HC04-D as an example to illustrate: (Q1 connection has been described briefly here. For more details, refer to Q1 Software Manual.)

1) Open CODESYS V3.5 SP14, select New project

The user can select the project type they want, enter the name and save path, and then click "OK"



3) Double click Device→Scan network, then select the Q1 device and click "OK"



4) After communicating with Q1 device, click Device \rightarrow Add device \rightarrow EtherCAT Master SoftMotion

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2) Follow the CODESYS guide, select the target device and main program PLC_PRG programming language. Q1 device is not installed by default, so you need to install the device description file first, otherwise the correct target device cannot be selected.

You are about to create a new standard project. This wizard will create the following objects within this project: - One programmable device as specified below - A program PLC_PRG in the language specified below - A cyclic task which calls PLC_PRG - A reference to the newest version of the Standard library currently installed. Select target device Device: HCQ1-1300-D (Zhejiang Hechuan Technology) PLC_PRG in: Structured Text (ST) Select programming language



5) Double click EtherCAT Master SoftMotion, and find the "Source Address (Mac)" under the "General" on the right and select the correct EtherCAT network card.

Untitled1 Device (HCO1-1300-D)	General Autoconfig Master/Slaves					EtherCAT
Denke (Full (Factor) Denke (Full (Factor) Denke (Full (Factor) Denke (Full (Factor) Denke (Factor) Den	Sync Unit Assignment EtherCAT I/O Mapping EtherCAT IEC Objects	EtherCAT Destinat Source A Select Network	NIC Setting ion Address (MAC) iddress (MAC) 0 Adapter	F-FF-FF-FF-FF 0-00-00-00-00-00	Broadcast Browse	2 Enable Redundancy
	Information 3	MAC address 00049F04E10 00049F04E10 00049F04E10	Name C3 eth0 C4 eth1 C5 ecat1	Description		
				OK		Abort

6) Right-click EtherCAT Master SoftMotion to select the scan device and for the module, which works normally and has established communication, find it in the "Scan device" and click the "Copy all devices to the project" in the lower right corner to add the module to the project.

	ж Па Па	Cut Copy Paste	Core Davier			
	×	Befactoring	Scan Devices			_ 0
	G	Properties	Devicename	Devicetype	Alias Address	
	*in	Add Object Add Folder Add Device Insert Device	HE-LocalEtherCATDevice	LocalEtherCATBevice HEO4 ,4Ch Counter, BC24V,Max Freq 200kHz	0	
		Scan For Devices				
	Disa Upo Edit Edit Imp Exp	Disable Device Update Device Edit Object Edit Object With				
		Edit IO mapping Import mappings from CSV Export mappings to CSV	Assign Address			Danow pillerences to Project
		Simulation	Scan Device		Copy All Devices	to Projec Close

7) Log in and run the program, select the module HCQX-HC04-D, and tick "Enable Expert Mode" in "General"

vevices v + A	PLC_PKG Devic	te EtherCAT_Master_S	orumouon		ncQx_nc04_02	×	
	General	Address			- Additional		
Device (connected) (HCQ1-1500-0) Device (HCQ1	Expert Process Data	AutoIncAddress EtherCAT Address	-1 1002	4	C Enable	Expert Settings	Ether CAT
Library Manager	Process Data	Distributed Clock					
ILC_PRG (PRG) Start Configuration	Startup Parameters	Select DC	DC-Synchro	n		~	
EtherCAT_Task	Online	🕑 Enable	4000	Sync Ur	nit Cycle (µs)		
E OB MainTask 一型 PLC_PRG	CoE Online	Sync0:					
SoftMation General Avis Roal	EtherCAT I/O Mapping	Sync Unit Cycle	× 1		4000	Cycle Time (µs	;)
G EtherCAT_Master_SoftMotion (EtherCAT Master SoftMotion) JoraEtherCATDavice (LocaEtherCATDavice)	EtherCAT IEC Objects	O User Defined			0 🗘	Shift Time (µs))
G HCQX_HC04_D2 (HC04, 4Ch Counter, DC24V, Mat	Status	Sync1: Enable Sync 1					
	Information	Sync Unit Cycle	× 1		4000 🌲	Cycle Time (µs	;)
		O User Defined			0 🔹	Shift Time (µs))
		Diagnostics					
		Current State	perational				

8) On the CoE online page 16#800C, set the corresponding channel Pluse Input Mode of ENC Pluse Input Mode to 2. For detailed parameter settings, please refer to the appendix.

Devices 👻 🕈 🗙	PLC_PRG Device	EtherCAT_Master	_SoftMotion HCQX_HC04_D2	×		
Untitled1 Unitited1 Device (connected) (HCO1+1300-D)	General	Read Objects	Auto Update O Offline	from ESI File	O Online f	rom Device
Concellent (Interline (Inte	Expert Process Data Process Data Startup Parameters	Index:Subindex * 16#700F:16#00 * 16#8000:16#00 * 16#8001:16#00 * 16#8002:16#00	Name ENC Default Value Ch4 ENC Characteristic Value ENC External Input Function ENC Max Count Value Ch1	Flags	Туре	Value
	Online	 16#8003:16#00 16#8004:16#00 	ENC Min Count Value Ch1 ENC Max Count Value Ch2			
	CoE Online EtherCAT I/O Mapping	 16#8005:16#00 16#8006:16#00 	ENC Min Count Value Ch2 ENC Max Count Value Ch3			
	EtherCAT IEC Objects	 ■ 16#8007:16#00 ■ 16#8008:16#00 ■ 16#8009:16#00 	ENC Max Count Value Ch4 ENC Max Count Value Ch4 ENC Min Count Value Ch4			
	Status	* 16#800A:16#00	ENC Window			
	Information	* 16#800B:16#00	ENC Average Times ENC Pluse Input Mode			_
		:16#01	Pluse Input Mode Ch1	RW	USINT	2
		:16#02	Pluse Input Mode Ch2 Pluse Input Mode Ch3	RW	USINT	2 2
		:16#04	Pluse Input Mode Ch4	RW	USINT	2

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12

=) Untited I	General	Find	-	Add FB for IO	channel * Go	to ins		
O Device [connected] (HCQ1-1300-D)			and the second					
PLC Logic	Expert Process Data	Variable	Mapping Channel	Address	Type	Current Value	Prepared Value	Unit
- O Application [run]			ENC Operation Command Ch 1	%QW0	UINT	1		
Library Manager	Process Data		BitO	%QX0.0	BOOL	TRUE		
PLC_PRG (PRG)	Startup Parameters		Bit1	%QX0.1	BOOL	FALSE		
Task Configuration	biorcop r bronnetero		Bit2	%QX0.2	BOOL	FALSE		
EtherCAT_Task	Online	· •	Bit3	%QX0.3	BOOL	FALSE		
🖻 🥩 MainTask		- *	Bit4	%QX0.4	BOOL	FALSE		
DIC_PRG	CoE Online	- 10	Bit5	%QX0.5	BOOL	FALSE		
- 😯 🏅 LocalDevice		- **	Bit6	%QX0.6	BOOL	FALSE		
- 😳 🍐 SoftMotion General Axis Pool	EtherCAT I/O Mapping	- **	Bit7	%QX0.7	BOOL	FALSE		
😑 🧿 🗊 EtherCAT_Master_SoftMotion (EtherCAT Master SoftMotio	EtherCAT IEC Objects	-**	Bit8	%QX1.0	BOOL	FALSE		
O DecalEtherCATDevice (LocalEtherCATDevice)		- 10	Bit9	%QX1.1	BOOL	FALSE		
G HCQX_HC04_D2 (HC04 ,4Ch Counter,DC24V,Max	Status	- **	Bit 10	%QX1.2	BOOL	FALSE		
		- 10	Bit11	%QX1.3	BOOL	FALSE		
	Information		Bit12	%QX1.4	BOOL	FALSE		
		- **	Bit13	%QX1.5	BOOL	FALSE		
		- **	Bit14	%QX1.6	BOOL	FALSE		
			Bit15	%OX1.7	BOOL	FALSE		
		8-**	ENC Period Calculation Ch1	%Q82	USINT	0		
		8.74	ENC Latch Command Ch1	96084	LISINT	0		

10) At this time, in the EtherCAT I/O mapping interface, bit0 of the counter status ENC Timer Status changes to 1, and the current count value is stored in ENC Current Data.

evices • 4 ×	PLC_PRG M Device	EtherCA	Master_SoftMotion	HCQX_HC04_D2 X					
🗿 Untitled 1 💌	General	Find		Filter Show all			Add FB for IO chan	nel + Go to in	istanc
= O M Device [connected] (HCQ1-1300-D)	General								
PLC Logic	Expert Process Data	Variable	Mapping	Channel	Address	Туре	Current Value	Prepared Value	Un
 O Application [run] 		8-19		ENC Period Calculation Ch3	%QB26	USINT	0		
- 10 Library Manager	Process Data	8-9		ENC Latch Command Ch3	%QB28	USINT	0		
PLC_PRG (PRG)	Charles Decomplant	B- 🖗		ENC Default Value Ch3	%QD8	DINT	0		
Task Configuration	Startup Parameters	8-10		ENC Operation Command Ch4	%QW18	UINT	0		
- BetherCAT_Task	Online	18 - * ø		ENC Period Calculation Ch4	%QB38	USINT	0		
=-愛 ManTask 一団 PLC_PRG 		18- * ø		ENC Latch Command Ch4	%QB40	USINT	0		
	CoE Online	18 - * ø		ENC Default Value Ch4	%QD11	DINT	0		
		B- 🍫		ENC Current Data Ch1	%ID0	DINT	0		
- 😳 🏅 SoftMotion General Axis Pool	EtherCAT I/O Mapping	😑 - 🍫		ENC Timer Status Ch1	%IB4	USINT	1		
🖻 📀 💮 EtherCAT_Master_SoftMotion (EtherCAT Master SoftMoti	EtherCAT IEC Objects	- **		Bit0	%D(4.0	BOOL	TRUE		
= 😏 🗊 LocalEtherCATDevice (LocalEtherCATDevice)		- **		Bit1	%D(4.1	BOOL	FALSE		
G I HCQX_HC04_D2 (HC04 , 4Ch Counter, DC24V, Max	Status	- **		Bit2	%D(4.2	BOOL	FALSE		
		- **		Bit3	%DX4.3	BOOL	FALSE		
	Information	- **		Bit4	%D(4.4	BOOL	FALSE		
		- **		Bit5	%DX4.5	BOOL	FALSE		
		- **		Bit6	%D(4.6	BOOL	FALSE		
		- *>		Bit7	%DX4.7	BOOL	FALSE		
		18 - Mp		ENC External Input Status Ch1	%IB6	USINT	0		
		(i) - Mp		ENC Period Status Ch1	%IB8	USINT	0		
		10 - Mp		ENC External Latch Data Ch1	%ID3	DINT	0		
		18 - No		ENC Software Latch Data Ch1	%ID4	DINT	0		

Appendix: Object dictionary

Object	Subindex	Name	Attribute	Type	Range	Default	Remark
dictionary	0	Deliver		UDINT.		100	
0x1000	0	Device type	R	UDINI		402	
0x1008	0	Device name	R	STRING			HCQX_HC04-D2
0x1009	0	Hardware version	R	STRING		0.7	
0x100A	0	Software version	R	STRING		5.1	
	00	Object identify					
0x1018	01	Supplier ID	R	UDINT		9	
	02	Product Code	R	UDINT		37458	
	03	Revised No.	R	UDINT		1	
	04	Serial No.	R	UDINT		1	
Ch1		·					
		Operation command	UINT			R	
		CENn	DIT	0	1	DAV	1: Counter enabled
	0	Counter enabled	BH	0	I or U	R/W	0: Counter disabled
	1	INRSn	DIT	0	1 or 0	DAM	0-1: Paset the current counter value
		Software (built-in) reset	DII	0	1010	FV VV	0-1. Reset the current counter value
	2	INLAn	BIT	0	1 or 0	R/W	0~1. Internal latch enabled
	-	Software latch		•	1 01 0		
	3	PSETn	BIT	0	1 or 0	R/W	0~1: Set the current counter value to the preset
		Software preset					value
	4	ERENn External reset enabled	BIT	0	1 or 0	R/W	1: External terminal reset enabled
0x 7000		ZCD=					External terminal reset disabled
	5	Z-phase reset enabled	BIT	0	1 or 0	R/W	0: Z-phase reset disabled
		EPCPp					o. 2 phase reset disabled
	6	External reset complete flag	BIT	0	1 or 0	R/W	0~1: Clear external reset complete flag
	-	cleared		-			- ···
	7	ZSCRn Z-phase	DIT	0	1 or 0	DAM	0-1: Clear Z-phase reset complete flag
	'	reset complete flag cleared	DII	0	1010	FV VV	0.41. Clear z-phase reset complete hag
	8	UPCRn	BIT	0	1 or 0	R/W	0~1: Elag cleared
	0	Clear upper limit flag	DIT	0	1010	1011	o n. hag dealed
	9	DOWNCRn	BIT	0	1 or 0	R/W	0~1: Flag cleared
		Clear lower limit flag		-			
		Pulse period measurement	USNII			к	
	1	PPENn Bulas period measurement	DIT	0	1 or 0	DAM	1: Pulse period measurement enabled
		enabled	DII	0	1010	PV VV	0: Pulse period measurement disabled
07001		PPCARn					
0x7001	2	Pulse period measurement	BIT	0	1 or 0	R/W	0~1: Pulse period measurement value cleared
		value clear					
	-	PPOFn					
	3	Pulse period measurement	BH	0	1 or 0		0~1: Pulse period measurement over limit flag clear
	-	Lateh function	LICINIT			D	
		Laten function	USINI			к	1. Euternal lately is suit as also
	1	External latch input enabled	BIT	0	1 or 0	R/W	0: External latch input disabled
		LTDC=					0: Trigger appo
	2	External latch trigger	BIT	0	1 or 0	R/W	1: Trigger continuously
0x7002	-	condition		-			The effective time is that LENn , change from 0 to 1
							0: External input
		LSELn	DIT			5.44	1: Z phase of the channel
	3	Latch input terminal	BH	0	1 or 0	R/W	The effective time is that LENn , change from 0 to 1.
		selection					function of phase Z is disabled
				-	214748368		
0x/003		Preset value	DINT	0	~214748367	R/W	
Ch2							
		Operation command	UINT			R	
0x7004		CENn	DIT			B.444	1: Counter enabled
	U	Counter enabled	BH	U	i or U	K/W	0: Counter disabled
	1	INRSn	DIT	0	1 or 0	DAM	0.1. Posset the surrent counter value
		Software (built-in) reset		J	1010	IV W	
	2	INLAn	RIT	0	1 or 0	RAW	0~1: Internal latch enabled
	2	Software latch	DIT	•	1010	10 11	o T. Internaliater enabled
	3	PSETn	BIT	0	1 or 0	R/W	0'1: Set the current counter value to the preset
	-	Software preset		-		ł	
	4	EKENN External resol enabled	BIT	0	1 or 0	R/W	External reset enabled External reset disabled
		ZSCRn	<u> </u>			ł	Liternal reset disabled T phase reset enabled
	5	Zoukii Zonhase reset enabled	BIT	0	1 or 0	R/W	 Z-phase reset enabled Z-phase reset disabled
		FRCRn				<u> </u>	
	6	External reset complete flag	BIT	0	1 or 0	R/W	0~1: Clear external reset complete flag
		cleared					

9) On the EtherCAT I/O mapping interface, set bit0 in the ENC Operation Command to 1. For detailed parameter settings, please refer to the appendix.

dictionary	Subindex	Name	Attribute	Туре	Range	Default	Remark
0x7004	7	ZSCRn Z-phase reset complete flag cleared	BIT	0	1 or 0	R/W	0~1: Clear Z-phase reset complete flag
	8	UPCRn Clear upper limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
	9	DOWNCRn Clear lower limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
0x7005		INLAn Pulse period measurement	USNIT			R	
	1	PPENn Pulse period measurement enabled	BIT	0	1 or 0	R/W	 Pulse period measurement enabled Pulse period measurement disabled
	2	PPCARn Pulse period measurement value clear	BIT	0	1 or 0	R/W	0~1: Pulse period measurement value cleared
	3	PPOFn Pulse period measurement overlimit flag clear	BIT	0	1 or 0		0~1: Pulse period measurement overlimit flag clear
		Latch function	USINT	0	1 or 0	R	
	1	LENn External latch input enabled	BIT	0	1 or 0	R/W	1: External latch input enabled 0: External latch input disabled
0x7006	2	LTRGn External latch trigger condition	BIT	0	1 or 0	R/W	0: Trigger once 1: Trigger continuously The effective time is that LENn , change from 0 to 1
	3	LSELn Latch input terminal selection	BIT	0	1 or 0	R/W	0: External input 1: Z phase of the channel The effective time is that LENn , change from 0 to 1. If the latch terminal selects phase Z, the reset function of phase Z is disabled
0x7007		Preset value	DINT	0	214748368 ~214748367	R/W	
Ch3							
		Operation command	UINT			R	
	0	CENn Counter enabled	BIT	0	1 or 0	R/W	1: Counter enabled 0: Counter disabled
	1	INRSn Software (built-in) reset	BIT	0	1 or 0	R/W	0~1: Reset the current counter value
	2	INLAn Software latch	BIT	0	1 or 0	R/W	0~1: Internal latch enabled
	3	PSETn Software preset	BIT	0	1 or 0	R/W	0~1: Set the current counter value to the preset value
	4	ERENn External reset enabled	BIT	0	1 or 0	R/W	1: External terminal reset enabled 0: External terminal reset disabled
0x7008	5	ZSCRn Z-phase Reset enabled	BIT	0	1 or 0	R/W	1: Z-phase reset enabled 0: Z-phase reset disabled
	6	ERCRn External reset complete flag cleared	BIT	0	1 or 0	R/W	0~1: Clear external reset complete flag
	7	ZSCRn Z-phase reset complete flag cleared	BIT	0	1 or 0	R/W	0~1: Clear Z-phase reset complete flag
	8	UPCRn Clear upper limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
	9	DOWNCRn Clear lower limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
		Pulse period measurement	USNIT			R	
	1	PPENn Pulse period measurement enabled	BIT	0	1 or 0	R/W	 Pulse period measurement enabled Pulse period measurement disabled
0x7009	2	PPCARn Pulse period measurement value clear	BIT	0	1 or 0	R/W	0~1: Pulse period measurement value cleared
	3	PPOFn Pulse period measurement overlimit flag clear	BIT	0	1 or 0		0~1: Pulse period measurement overlimit flag clear
0x700A		Pulse period measurement	USINT			R	
	1	PPENn Pulse period measurement enabled	BIT	0	1 or 0	R/W	 Pulse period measurement enabled Pulse period measurement disabled
	2	PPCARn Pulse period measurement value clear	BIT	0	1 or 0	R/W	0~1: Pulse period measurement value cleared
	3	PPOFn Pulse period measurement overlimit flag clear	BIT	0	1 or 0	R/W	0~1: Pulse period measurement overlimit flag clear
					-214748368	DAV	

Object dictionary	Subindex	Name	Attribute	Туре	Range	Default	Remark
Ch4							
	0	Operation command CENn	UINT	0	1 or 0	R	1: Counter enabled
		Counter enabled	511	-	1010	10.44	0: Counter disabled
	1	Software (built-in) reset	BIT	0	1 or 0	R/W	0~1: Reset the current counter value
	2	Software latch	BIT	0	1 or 0	R/W	0~1: Internal latch enabled
	3	PSETn Software preset	BIT	0	1 or 0	R/W	0~1: Set the current counter value to the preset value
0v700C	4	ERENn External reset enabled	BIT	0	1 or 0	R/W	1: External reset enabled 0: External reset disabled
0x700C	5	ZSCRn Z-phase reset enabled	BIT	0	1 or 0	R/W	1: Z-phase reset enabled 0: Z-phase reset disabled
	6	ERCRn External reset complete flag cleared	BIT	0	1 or 0	R/W	0~1: Clear external reset complete flag
	7	ZSCRn Z-phase reset complete flag cleared	BIT	0	1 or 0	R/W	0~1: Clear Z-phase reset complete flag
	8	UPCRn Clear upper limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
	9	DOWNCRn Clear lower limit flag	BIT	0	1 or 0	R/W	0~1: Flag cleared
		Pulse period measurement	USNIT			R	
	1	PPENn Pulse period measurement enabled	BIT	0	1 or 0	R/W	 Pulse period measurement enabled Pulse period measurement disabled
0x700D	2	PPCARn Pulse period measurement value clear	BIT	0	1 or 0	R/W	0~1: Pulse period measurement value cleared
	3	PPOFn Pulse period measurement overlimit flag clear	BIT	0	1 or 0		0~1: Pulse period measurement over limit flag clear
		Latch function	USINT			R	A. E. C. State I. S. State I. S. State I.
	1	External latch input enabled	BIT	0		R/W	0: External latch input enabled
0x700E	2	LTRGn External latch trigger condition	BIT	0	1 or 0	R/W	0: Trigger once 1: Trigger continuously The effective time is that LENn , change from 0 to 1
	3	LSELn Latch input terminal selection	BIT	0	1 or 0	R/W	0: External input 1: Z phase of the channel The effective time is that LENn , change from 0 to 1. If the latch terminal selects phase Z, the reset function of phase Z is disabled
0x700F		Preset value	DINT	0	-214748368 ~214748367	R/W	
	0	Synchronization manager			214/40007		
	1	Communication type SM0	R	USINT		1	
0x1C00	2	Communication type SM01	R	USINT		2	
	3	Communication type SM2	R	USINT		3	
	4	Communication type SM3	R	USINT		4	
0v1C12	0	Sync Manager 2PDO distribution					
0,1012	01~10	PDO mapping	R	UINT		5632~5647	
	0	Sync Manager 3PDO					
0x1C13	01~21	PDO mapping	R	LIINT			
0x8000	0	Characteristic Parameters	IX.	UNIT			
	1	I1 logic state selection	R/W	BOOL	0 or 1	0	0: Normally-open 1: Normally-closed
	2	12 logic state selection	R/W	BOOL	0 or 1	0	0: Normally-open 1: Normally-closed
	3	13 logic state selection	R/W	BOOL	0 or 1	0	0: Normally-open 1: Normally-closed
	4	14 logic state selection	R/W	BOOL	0 or 1	0	0: Normally-open 1: Normally-closed
	5	Channel 1 Counting type	R/W	BOOL	0 or 1	0	0: Ring counter 1: linear counter
	6	Channel 2 Counting type	R/W	BOOL	0 or 1	0	0: Ring counter 1: linear counter
	7	Channel 3 Counting type	R/W	BOOL	0 or 1	0	0: Ring counter 1: linear counter
	8	Channel 4 Counting type	R/W	BOOL	0 or 1	0	0: Ring counter 1: linear counter
	9	Channel 1 Encoding counting direction	R/W	BOOL	0 or 1	0	0: A phase as the positive direction 1: B phase as the positive direction

dictionary	Subindex	Name	Attribute	Туре	Range	Default	Remark
0x8000	0A	Channel 2 Encoding counting direction	R/W	BOOL	0 OR 1	0	0: A phase as the positive direction 1: B phase as the positive direction
	0B	Channel 3 Encoding counting direction	R/W	BOOL	0 OR 1	0	0: A phase as the positive direction 1: B phase as the positive direction
	0C	Channel 4 Encoding counting direction	R/W	BOOL	0 OR 1	0	0: A phase as the positive direction 1: B phase as the positive direction
0x8001	0	External pin function					
	1	11 logic state selection	R/W	USINT	0 ~ 5	0	0: Disable 1: General input 2: Latch input 3: Gate input 4: Preset input 5: Reset input
	2	I2 logic state selection	R/W	USINT	0~5	0	0: Disable 1: General input 2: Latch input 3: Gate input 4: Preset input 5: Reset input
	3	13 logic state selection	R/W	USINT	0 ~ 5	0	0: Disable 1: General input 2: Latch input 3: Gate input 4: Preset input 5: Reset input
	4	14 logic state selection	R/W	USINT	0 ~ 5	0	0: Disable 1: General input 2: Latch input 3: Gate input 4: Preset input 5: Reset input
0x8002	0	Ch1 Max. value	R/W	DINT	1~2147483647	2147483647	
0x8003	0	Ch1 Mini. value	R/W	DINT	-2147483647~0	-2147483647	
0x8004	0	Ch2 Max. value	R/W	DINT	1~2147483647	2147483647	
0x8005	0	Mini. value Ch2 index address					
	1	Mini. value	R/W	DINT	-2147483647~0	-2147483647	
0x8006	0	Ch3 Max. value	R/W	DINT	1~2147483647	2147483647	
0x8007	0	Ch3 Mini, value	R/W	DINT	-2147483647~0	-2147483647	
0x8008	0	Ch4 Max. value	R/W	DINT	1~2147483647	2147483647	
0x8009	0	Ch4 Mini, value	R/W	DINT	-2147483647~0	-2147483647	
	0	Speed measurement window					
0x800A	1	Pulse rate measurement Time window	R/W	UINT	0~65535	0	When the setting is not 0, the pulse rate measurement function is turned on. Unit: ms
0x800B 0x800C	0	Speed measurement average times					
	1	Pulse rate measurement Average times	R/W	INT	0~100	0	When the setting is not 0, the average times is turned on. Unit: Times
	0	Pulse input mode					
	1	Channel 1 Pulse input mode	R/W	USINT	1~4	2	0: Not supported 1: ×2 orthogonal phase pulse 2: ×4 orthogonal phase pulse 3: Pulse + direction 4: Up/down pulse
	2	Channel 2 Pulse input mode	R/W	USINT	1~4	2	0: Not supported 1: ×2 orthogonal phase pulse 2: ×4 orthogonal phase pulse 3: Pulse + direction 4: Up/down pulse
	3	Channel 3 Pulse input mode	R/W	USINT	1~4	2	0: Not supported 1: ×2 orthogonal phase pulse 2: ×4 orthogonal phase pulse 3: Pulse + direction 4: Up/down pulse
	4	Channel 4 Pulse input mode	R/W	USINT	1~4	2	0: Not supported 1: ×2 orthogonal phase pulse 2: ×4 orthogonal phase pulse 3: Pulse + direction 4: Undrown pulse

